

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>In re U.S. Patent Application of</b>	)
	)
<b>SATOU et al.</b>	)
	)
<b>Application Number: To be Assigned</b>	)
	)
<b>Filed: Concurrently Herewith</b>	)
	)
<b>For: SEMICONDUCTOR INTEGRATED CIRCUIT AND</b>	)
<b>DATA PROCESSING SYSTEM</b>	)
	)
<b>ATTORNEY DOCKET NO. HITA.0386</b>	)

**Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231**

**INFORMATION DISCLOSURE STATEMENT**

The above-referenced application is a divisional of U.S. Serial No. 10/416,709 filed on May 14, 2003, which is a national stage application of PCT/JP00/08443 filed on November 30, 2000. It includes the same disclosure as U.S. patent application Serial No. 10/416,709.

It is understood that the listed references will be considered in the examination of the application and that no separate copies of the same prior art are required to be provided since they were previously cited or transmitted in the foregoing prior application, pursuant to 37 CFR § 1.98(d). Form(s) PTO 1449 is enclosed listing references cited by the Examining Attorney and submitted by applicant in the prior applications.

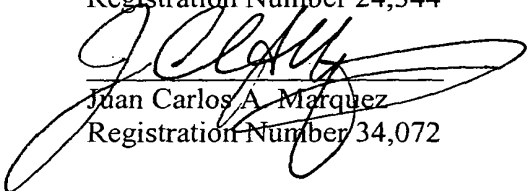
This Information Disclosure Statement is submitted with the above-captioned U.S. divisional application. Accordingly, no fee is due or payable at this time.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

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